

**Notice of Allowance Mailed: December 18, 2006**  
**Confirmation No. 3078**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	S. SRINIVASAN et al.	Examiner:	Mark A. Radtke
Serial No.:	10/647,997	Group Art Unit:	2165
Filed:	August 25, 2003	Docket No.:	SVL920030042US1
TITLE:	METHOD, SYSTEM, AND ARTICLE OF MANUFACTURE FOR PARALLEL PROCESSING AND SERIAL LOADING OF HIERARCHICAL DATA		

---

**CERTIFICATE UNDER 37 CFR 1.8:**

I hereby certify that this correspondence is being transmitted through the USPTO EFS-Web system over the Internet to Mark A. Radtke of the U.S. Patent and Trademark Office on March 7, 2007

/Janaki K. Davda/  
Janaki K. Davda Reg. No. 40,684

**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The Examiner may have provided various reasons for allowance. Applicants will not specifically address or respond herein to any issues/points raised by the Examiner in any Examiner's provided remarks. However, this should not necessarily be viewed as constituting acquiescence by Applicants as to the correctness of some or all of the Examiner's provided remarks.

Moreover, Applicants note that the claims are directed to various combinations of features. It is respectfully submitted that the patentability of each of the allowed and allowable claims resides in every feature of the recited combination of features of the claims in addition to any features noted by the Examiner.

The attorney of record invites the Examiner to contact her at (310) 553-7973 if the Examiner believes such contact would advance the prosecution of the case.

Dated: March 7, 2007

By:           /Janaki K. Davda/          

Janaki K. Davda  
Registration No. 40,684

Please direct all correspondences to:

David Victor  
Konrad Raynes & Victor, LLP  
315 South Beverly Drive, Ste. 210  
Beverly Hills, CA 90212  
Tel: 310-553-7977  
Fax: 310-556-7984